ABSTRACT OF THE DISCLOSURE

An image processing apparatus applied to a digital
camera, includes: a frame memory that inputs and stores, via
5   a bus, image data obtained by capturing an optical image
through an optical system by a CCD; a first data-order
converting unit that reads and inputs the image data from
the frame memory in units of a block in the row direction,
and converts and outputs the image data in the column
10 direction; an image processing unit that is connected to the
first data-order converting unit for pipeline processing and
performs image processing; a distortion correction
processing unit that is connected to the image processing
unit for pipeline processing and performs the distortion
15 correction processing; and a second data-order converting
unit that is connected to the distortion correction
processing unit for pipeline processing, returns block data
in the column direction into that in the row direction, and
outputs the converted data to the frame memory.